



INFORMATION DISCLOSURE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449) (Substitute)	Docket No.: 872.0182.U1(US)	Serial No.: 10/815,887
	Applicant(s): Kunnari et al.	
	Filing Date: March 31, 2004	Group: 2631

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
/RG/	US-2003/0152154 A1 US- US- US- US- US- US- US- US- US- US- US-	08-14-2003	Johnson	375	259

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

/RG/	ROINE, P.T., "A System for Asynchronous High-speed Chip to Chip Communication", March 18-21 1996, Advanced Research in Asynchronous Circuits and Systems, 2 nd International Symposium, pgs. 2-10
/RG/	SMITH, R. et al., "High_Level Design for Asynchronous Logic", 2001, Design Automation Conference, Proceedings of the ASP-DAC 2001, Asia and South Pacific, pgs. 431-436
/RG/	TEIFEL, J. et al., "A High-Speed Clockless Serial Link Transceiver", May 12-15 2003, Asynchronous Circuits and Systems 2003 Proceedings, 9 th International Symposium, pgs. 1-11
/RG/	YANG, C-K K. et al., "Analysis of Timing Recovery for Multi-Gbps PAM Transceivers", September 21-24 2003, Custom Integrated Circuits Conference, pgs. 67-72

Examiner's Signature: /Rahel Guarino/ Date Considered: 07/12/2007

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